

ENGINEERING CHANGE ORDER

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ECO # 8969 P1 of 3TITLE MPI PLUS/CLASSIC PCB REVISIONREQUESTOR RAWNY DATE 10/19/94 APPROVED DENIED BY DTT DATE 10/21/94PART OR ASSEMBLY AFFECTED MAIN PC BOARD 680120PRODUCT MPI PLUS/CLASSIC

DESCRIPTION OF PROBLEM

CLOCK NOISE PRESENT IN AUDIO SIGNAL. THIS IS CAUSED BY COUPLING OF ADJACENT TRACES NEAR U33.

CHANGES

CHANGE PCB TO REROUTE TRACE FROM U33 PIN 40 TO U29 PIN 6 AWAY FROM TRACE CONNECTED TO U33 PIN 24. REROUTE ALL DIGITAL TRACES FARTHER FROM EQ CIRCUITRY.

REWORK (ATTACH SEPARATE SHEET IF NECESSARY) see attached dwgs

- ① ON COMPONENT SIDE OF PCB BOARD, CUT TRACE FROM END OF R211 CLOSEST TO Q 37. CUT TRACE CLOSE TO R211, PREFERABLY RIGHT AT PAD.
- ② ON SOLDER SIDE OF PCB CUT TRACE AT U33 PIN 40 RIGHT NEXT TO THE IC SOCKET PIN.
- ③ ON SOLDER SIDE OF PCB, CONNECT A 2 1/8" WIRE FROM U33 PIN 24 TO U29 PIN 6. USE THIN INSULATED WIRE SUCH AS TEFLON WIRE WRAP WIRE #26AWG.
- ④ Add RTV to fasten jumper & to keep it from getting pierced by IC leads.

EFFECTIVITY: NEW PRODUCTION WORK IN PROGRESS FINISHED GOODS RETURN TO STOCK DOCUMENTATION CHANGE ONLYBEGINNING SERIAL NUMBER All UNITS

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